



- ★ Super Low Gate Charge
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary

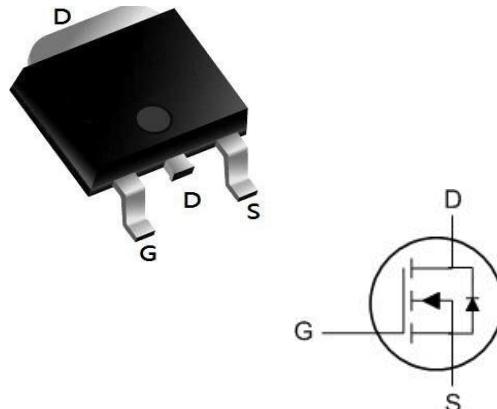
BVDSS	RDS(on)	ID
100V	18 mΩ	50A

Description

The XXW50N10 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDS(on) and gate charge for most of the synchronous buck converter applications .

The XXW50N10 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

TO252 Pin Configuration



Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
V_{DSS}	Drain-Source Voltage		100	V
V_{GSS}	Gate-Source Voltage		± 20	V
I_D	Continuous Drain Current	$T_c = 25^\circ\text{C}$	50	A
		$T_c = 100^\circ\text{C}$	30	A
I_{DM}	Pulsed Drain Current ^{note1}		150	A
EAS	Single Pulsed Avalanche Energy ^{note2}		62.6	mJ
P_D	Power Dissipation	$T_c = 25^\circ\text{C}$	73	W
R_{eJC}	Thermal Resistance, Junction to Case		2.0	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	$^\circ\text{C}$

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}}=0\text{V}$,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=\pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	1.0	1.5	2.5	V
$R_{\text{DS}(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	-	18	28	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=10\text{A}$	-	22	32	$\text{m}\Omega$
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1.0\text{MHz}$	-	3727	-	pF
C_{oss}				180	-	pF
C_{rss}	Reverse Transfer Capacitance		-	148	-	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=30\text{V}$, $I_D=15\text{A}$, $V_{\text{GS}}=10\text{V}$	-	40	-	nC
Q_{gs}	Gate-Source Charge		-	6.2	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	28	-	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DS}}=30\text{V}$, $I_D=15\text{A}$, $R_G=1.8\Omega$, $V_{\text{GS}}=10\text{V}$	-	22	-	ns
t_r	Turn-on Rise Time		-	182	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time		-	80	-	ns
t_f	Turn-off Fall Time		-	142	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current		-	-	50	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	150	A
V_{SD}	Drain to Source Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=30\text{A}$	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	$\text{IF}=30\text{A}, \text{dI}/\text{dt}=100\text{A}/\mu\text{s}$	-	71	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	145	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : $T_J=25^\circ\text{C}$, $V_{\text{DD}}=50\text{V}$, $V_G=10\text{V}$, $L=0.5\text{mH}$, $R_g=25\Omega$, $I_{\text{AS}}=14.5\text{A}$

3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$

Typical Performance Characteristics

Figure 1: Output Characteristics

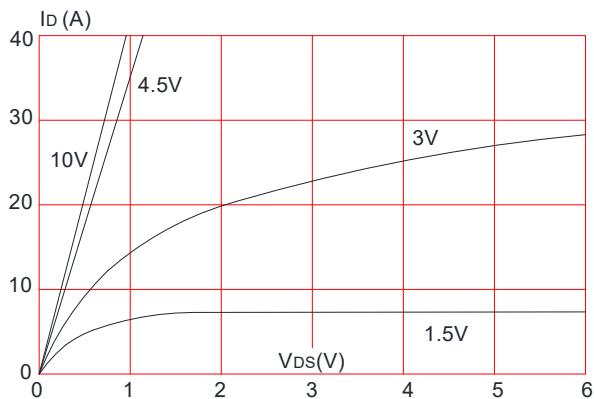


Figure 3: On-resistance vs. Drain Current

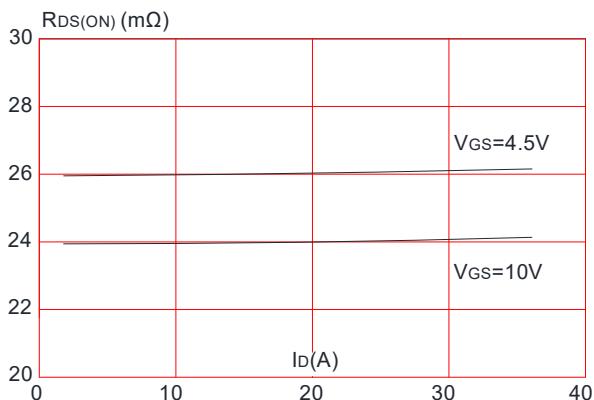


Figure 5: Gate Charge Characteristics

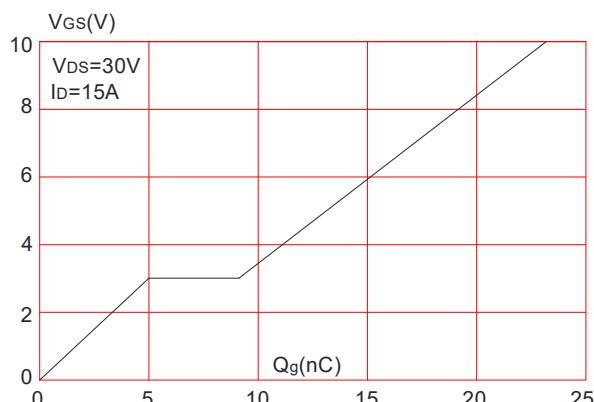


Figure 2: Typical Transfer Characteristics

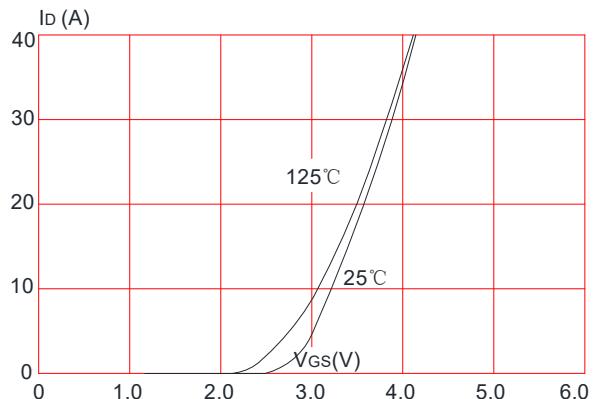


Figure 4: Body Diode Characteristics

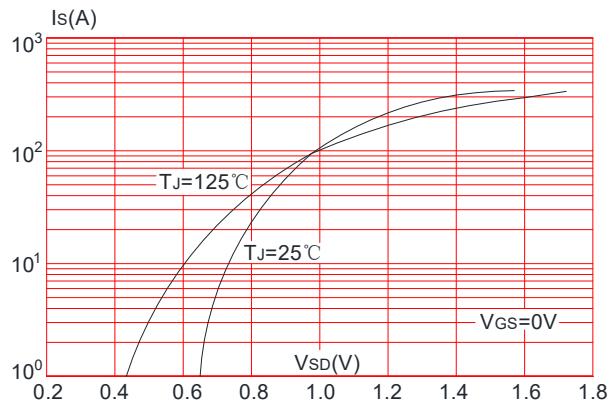


Figure 6: Capacitance Characteristics

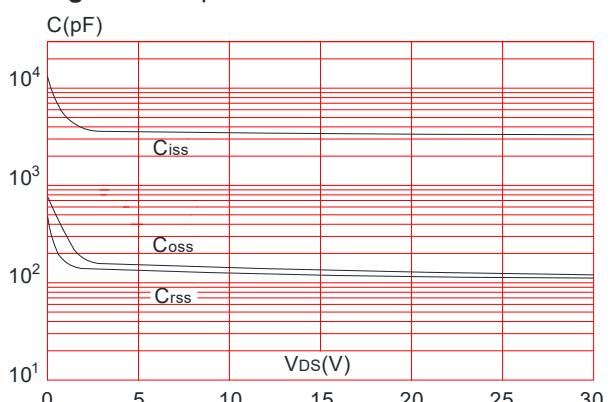


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

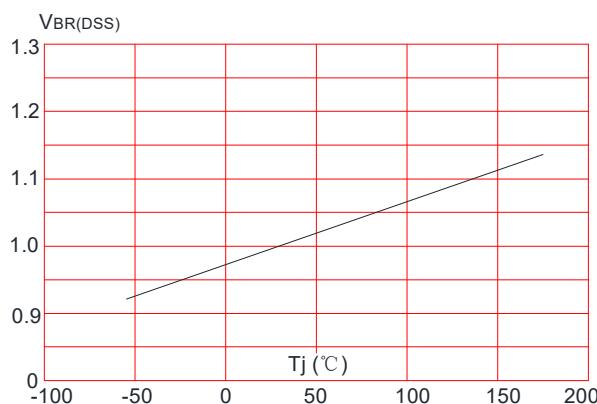


Figure 8: Normalized on Resistance vs. Junction Temperature

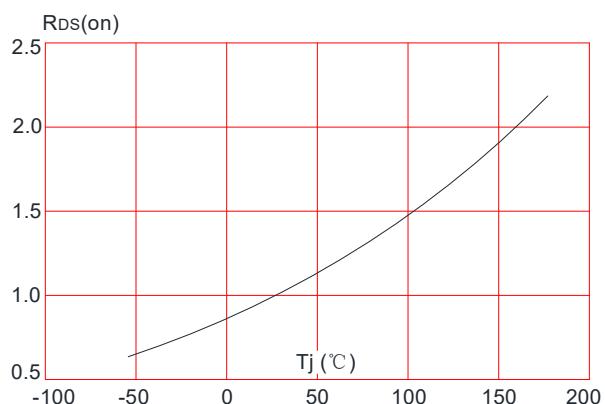


Figure 9: Maximum Safe Operating Area

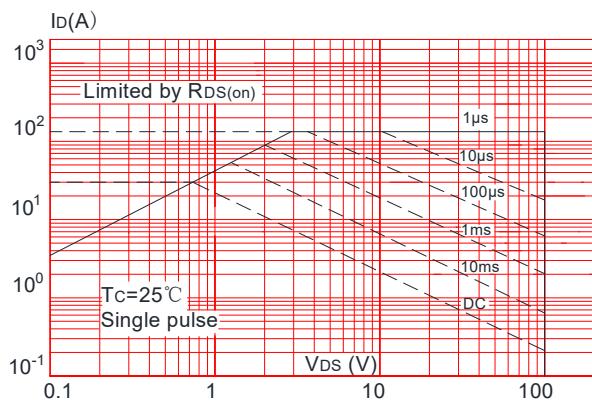


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

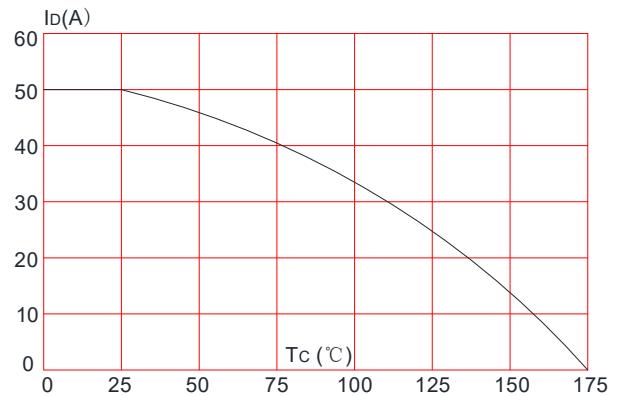
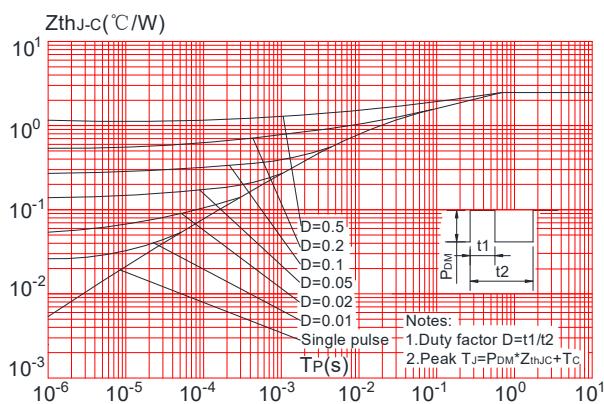
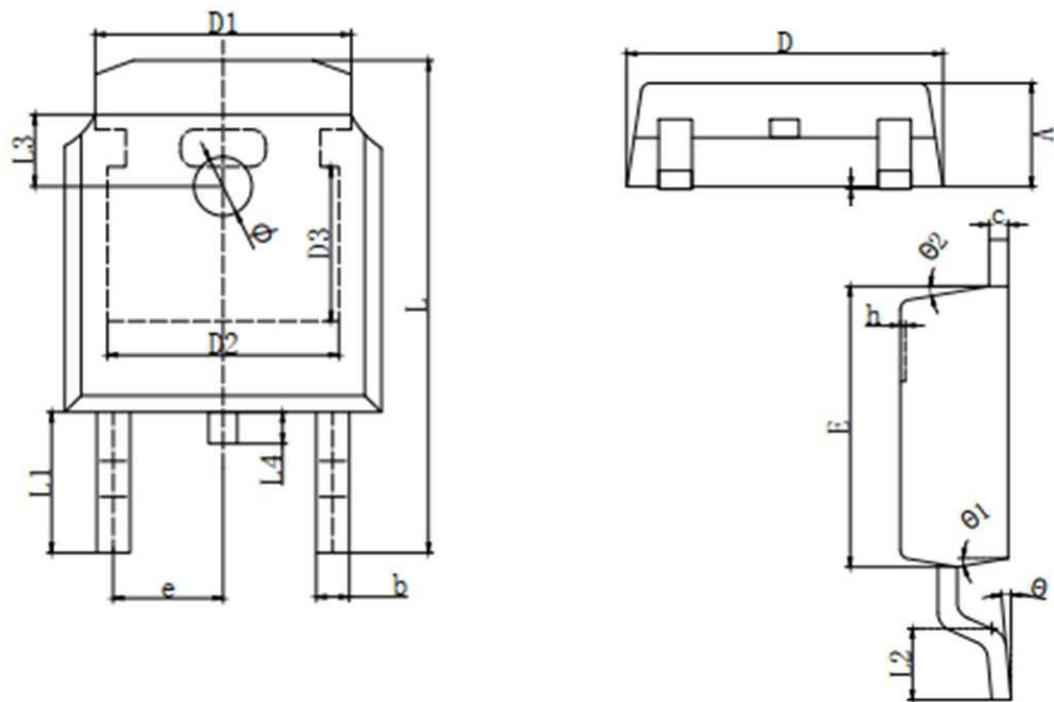


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



TO-252 Package outline



SYMBOL	MILLIMETER		SYMBOL	MILLIMETER	
	MIN	MAX		MIN	MAX
A	2.200	2.400	h	0.000	0.200
A1	0.000	0.127	L	9.900	10.30
b	0.640	0.740	L1	2.888 REF	
c	0.460	0.580	L2	1.400	1.700
D	6.500	6.700	L3	1.600 REF	
D1	5.334 REF		L4	0.600	1.000
D2	4.826 REF		phi	1.100	1.300
D3	3.166 REF		theta	0°	8°
E	6.000	6.200	theta1	9° TYP2	
e	2.286 TYP		theta2	9° TYP	