

XXWF15N65 Features: <ul style="list-style-type: none"> <input type="checkbox"/> Low Intrinsic Capacitances. <input type="checkbox"/> Excellent Switching Characteristics. <input type="checkbox"/> Extended Safe Operating Area. <input type="checkbox"/> Unrivalled Gate Charge :$Q_g = 48.5\text{nC}$ (Typ.). <input type="checkbox"/> $V_{DSS}=650\text{V}, I_D=15\text{A}$ <input type="checkbox"/> $R_{DS(on)} : 0.52 \Omega$ (Max) @ $V_G=10\text{V}$ <input type="checkbox"/> 100% Avalanche Tested 	  TO-220F  <p>1.Gate (G) 2.Drain (D) 3.Source (S)</p>
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Absolute Maximum Ratings ($T_a=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain-Source Voltage		650	V
I_D	Drain Current	$T_c=25^\circ\text{C}$	15	A
		$T_c=100^\circ\text{C}$	9.5	
V_{GSS}	Gate - Source voltage		± 30	V
E_{AS}	Single Pulse Avalanche Energy (note1)		920	mJ
I_{AR}	Avalanche Current (note2)		15	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)		60	W
T_j	Junction Temperature(Max)		150	$^\circ\text{C}$
T_{stg}	Storage Temperature		-55~+150	
TL	Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds		300	

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JC}$	Thermal Resistance,Junction to Case	-	2.08	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance,Junction to Ambient	-	62.5	

Electrical Characteristics (Ta=25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0	650	-	-	V
△BV _{DSS} /△T _J	Breakdown Voltage Temperature Coefficient	I _D =250μA, Reference to 25°C	-	0.71	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =650V, V _{GS} =0V	-	-	10	μA
		V _{DS} =480V, T _c =125°C			100	
I _{GSSF}	Gate-body leakage Current, Forward	V _{GS} =+30V, V _{DS} =0V	-	-	100	nA
I _{GSSR}	Gate-body leakage Current, Reverse	V _{GS} =-30V, V _{DS} =0V	-	-	-100	
On Characteristics						
V _{GS(TH)}	Date Threshold Voltage	I _D =250μA, V _{DS} =V _{GS}	2	-	4	V
R _{DS(ON)}	Static Drain-Source On-Resistance	I _D =7.5A, V _{GS} =10V	-	0.45	0.52	Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0, f=1.0MHz	-	2450	-	pF
C _{oss}	Output Capacitance		-	295	-	
C _{rss}	Reverse Transfer Capacitance		-	23.6	-	
Switching Characteristics						
T _{d(on)}	Turn-On Delay Time	V _{DD} =325V, I _D =15A R _G =21.7Ω (Note 3,4)	-	65	140	ns
T _r	Turn-On Rise Time		-	125	260	
T _{d(off)}	Turn-Off Delay Time		-	105	220	
T _f	Turn-Off Rise Time		-	65	140	
Q _g	Total Gate Charge	V _{DS} =480V, V _{GS} =10V, I _D =15A (Note 3,4)	-	48.5	63	nC
Q _{gs}	Gate-Source Charge		-	14	-	
Q _{gd}	Gate-Drain Charge		-	21	-	
Drain-Source Diode Characteristics and Maximum Ratings						
I _s	Max. Diode Forward Current	-	-	-	15	A
I _{SM}	Max. Pulsed Forward Current	-	-	-	60	
V _{SD}	Diode Forward Voltage	I _D =15A	-	-	1.4	V
T _{rr}	Reverse Recovery Time	I _s =15A, V _{GS} =0V dI/dt=100A/μs (Note3)	496	-	-	nS
Q _{rr}	Reverse Recovery Charge		5.69	-	-	μC

Notes : 1, L=8.15mH, IAS=15A, VDD=50V, RG=25Ω, Starting TJ =25°C

2, Repetitive Rating : Pulse width limited by maximum junction temperature

3, Pulse Test : Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

4, Essentially Independent of Operating Temperature

Typical Characteristics

Figure 1. On-Region Characteristics

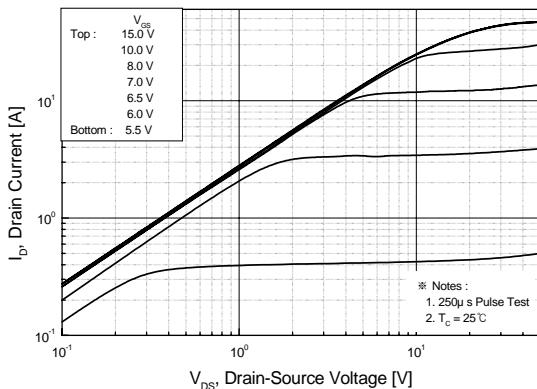


Figure 2. Transfer Characteristics

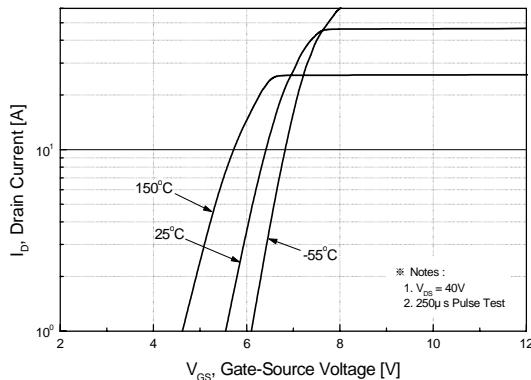


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

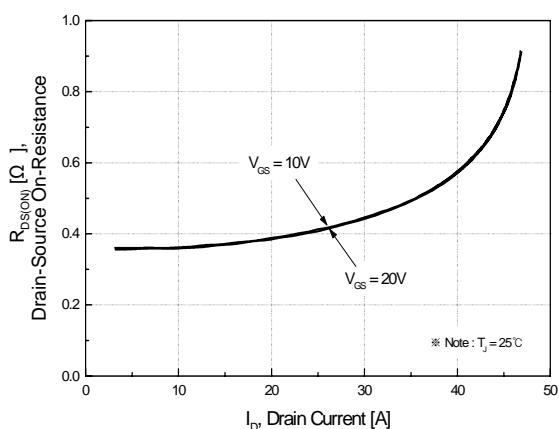


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

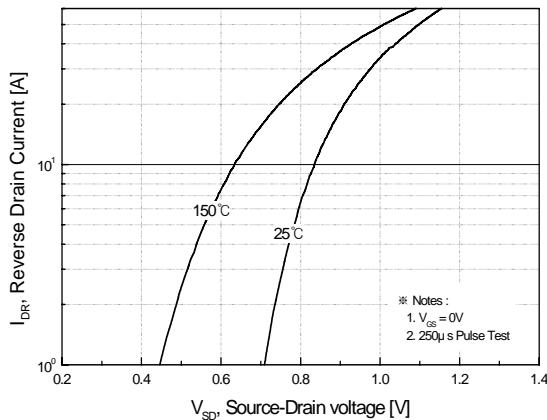


Figure 5. Capacitance Characteristics

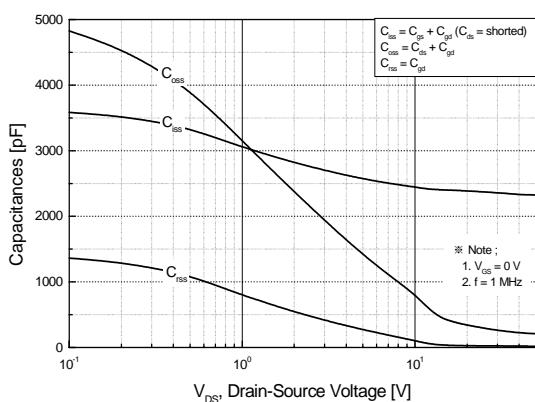
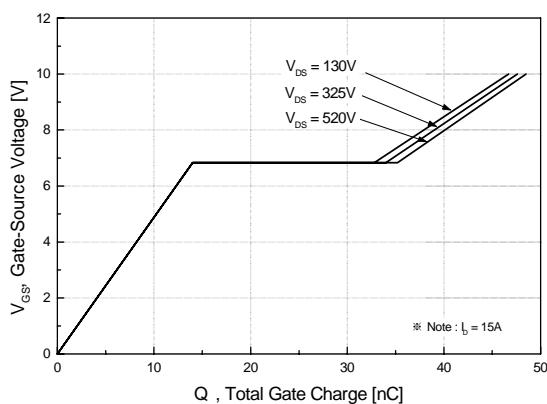
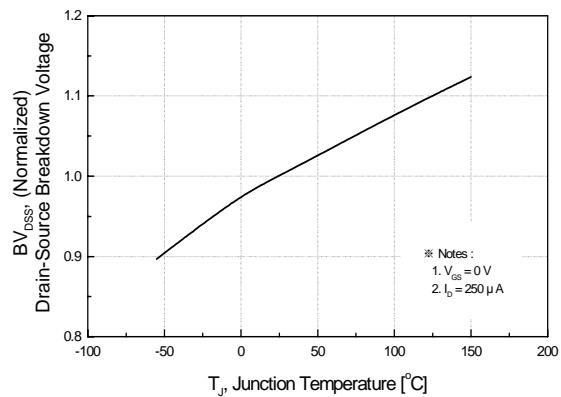
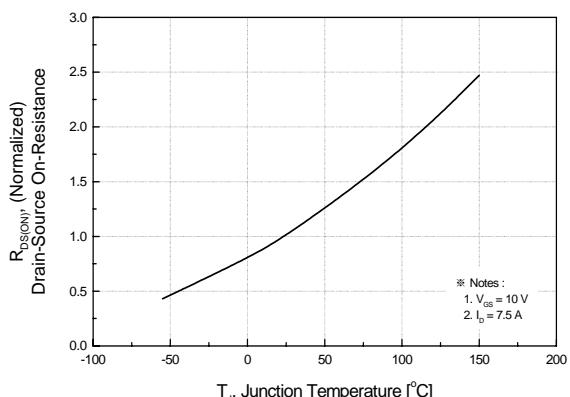
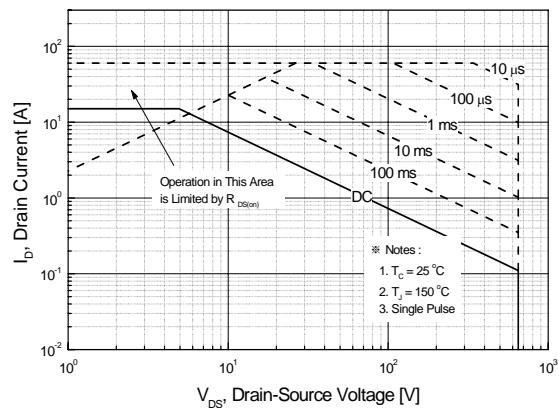
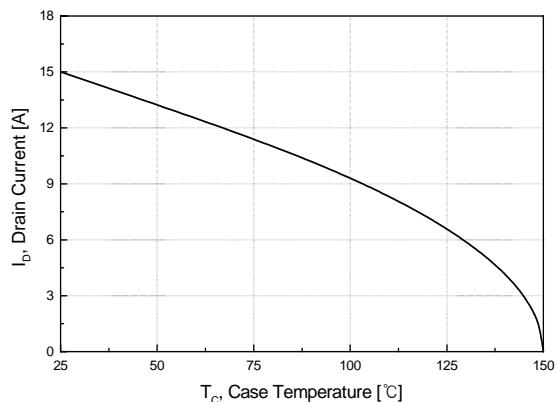
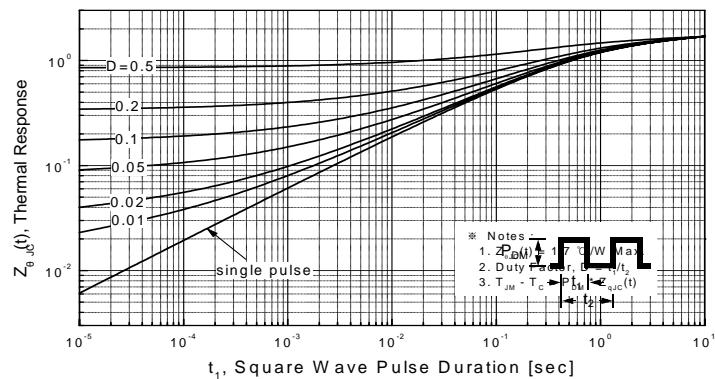
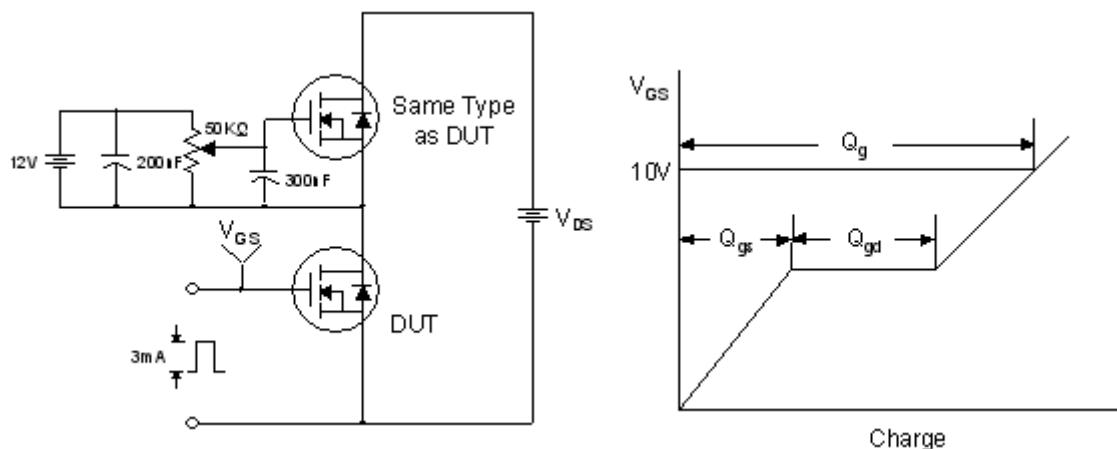
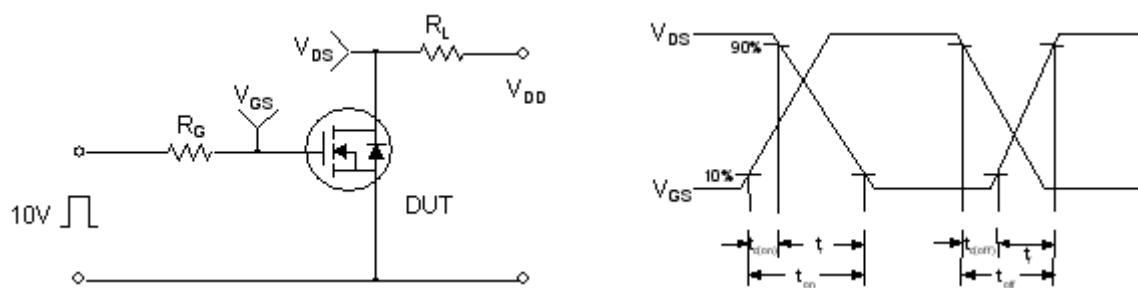
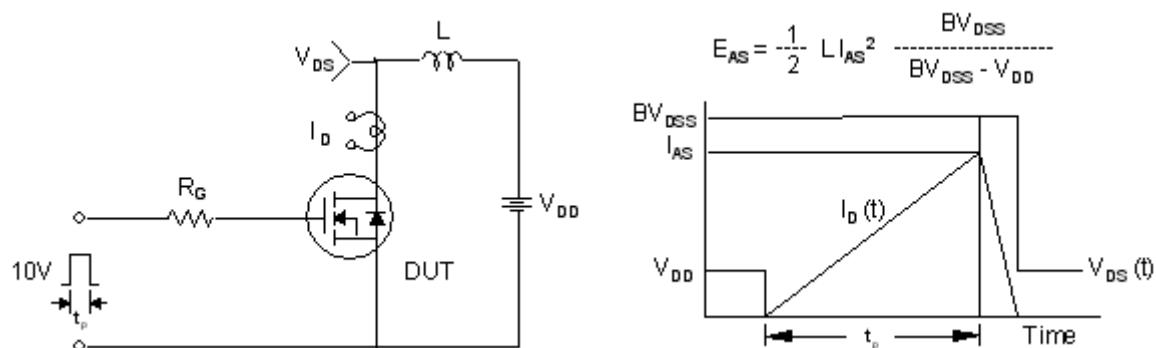


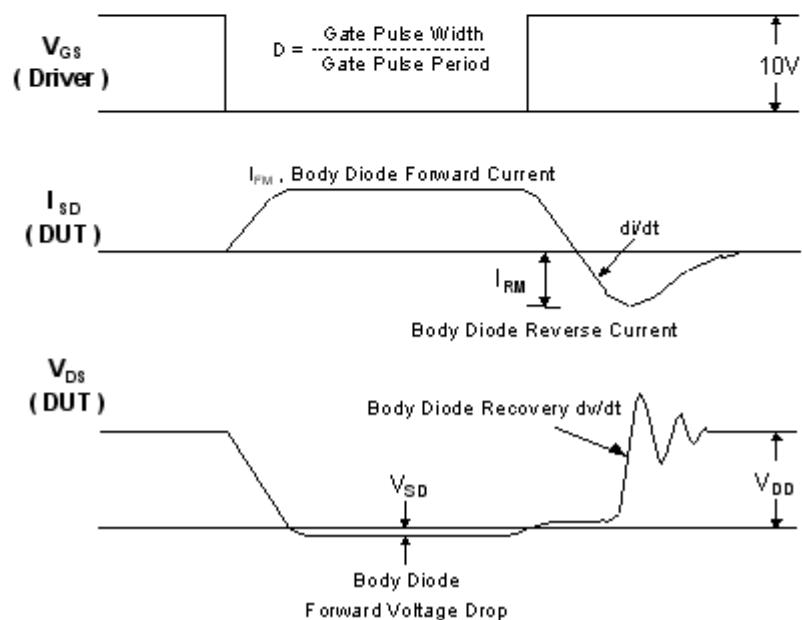
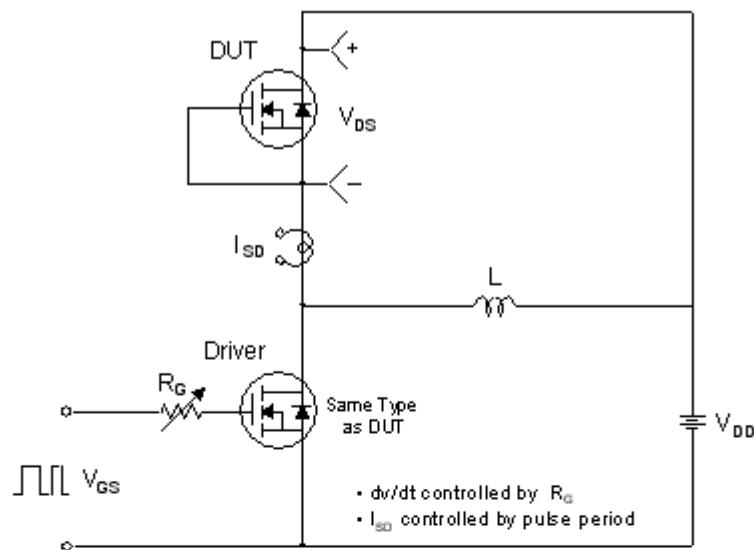
Figure 6. Gate Charge Characteristics



Typical Characteristics (Continued)
Figure 7. Breakdown Voltage Variation vs. Temperature

Figure 8. On-Resistance Variation vs. Temperature

Figure 9-2. Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

Figure 11-2. Transient Thermal Response Curve


Gate Charge Test Circuit & Waveform

Resistive Switching Test Circuit & Waveforms

Unclamped Inductive Switching Test Circuit & Waveforms


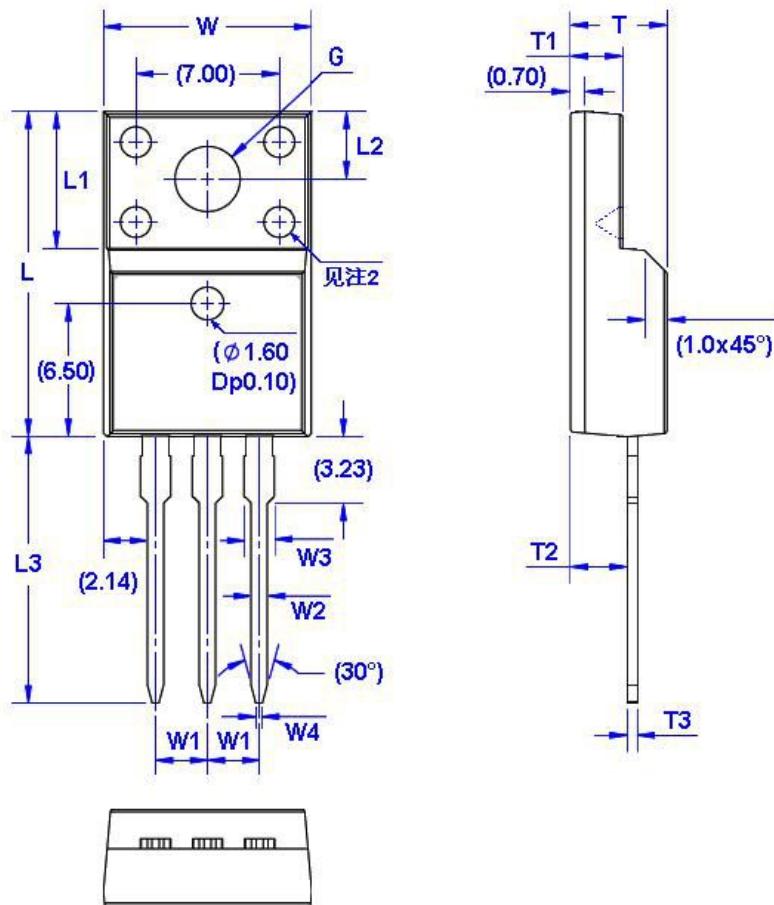
Peak Diode Recovery dv/dt Test Circuit & Waveforms



Package Dimension

TO-220F

Unit: mm



Symbol	Size		Symbol	Size		Symbol	Size		Symbol	Size	
	Min	Max		Min	Max		Min	Max		Min	Max
W	9.96	10.36	W4	0.25	0.45	L3	12.78	13.18	T3	0.45	0.60
W1	2.54 (TYP)		L	15.67	16.07	T	4.50	4.90	G(Φ)	3.08	3.28
W2	0.70	0.90	L1	6.48	6.88	T1	2.34	2.74			
W3	1.24	1.47	L2	3.20	3.40	T2	2.56	2.96			